



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2819

Examiner: Le, Don P.

Serial No.: 10/024,661

Filed: December 13, 2001

In re Application of: Khaled Ahmad El-Ayat

For: PROGRAMMABLE MULTI-STANDARD I/O ARCHITECTURE FOR FPGAs

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail, in an envelope addressed to Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 12/18/03.

Signed

*Stephanie Davis*  
Stephanie Davis

**REQUEST FOR EXTENSION OF TIME**

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Applicant requests an extension of time of one month to respond to the Office Action dated August 18, 2003. Enclosed is the requisite fee of \$110.00 as calculated pursuant to 37 C.F.R. §1.17(a-c) below:

	Small Entity	Std. Fee
One month extension	<input type="checkbox"/> \$ 55.00	<input checked="" type="checkbox"/> \$ 110.00
Two month extension	<input type="checkbox"/> \$210.00	<input type="checkbox"/> \$ 420.00
Three month extension	<input type="checkbox"/> \$475.00	<input type="checkbox"/> \$ 950.00

The Assistant Commissioner is hereby authorized to charge any additional fees or credit any overpayment to Deposit Account No.: 50-0612.

Respectfully submitted,  
SIERRA PATENT GROUP, LTD.

Kenneth D'Alessandro  
Reg. No. 29,144

12/23/2003 SZEWDIE1 00000157 10024661

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Dated: December 18, 2003

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